

IceBoot for IceRay

John Kelley
Univ. of Wisconsin, Madison
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1. Introduction

This document describes the commands added to the iceboot operating system for use with the IceRay testbed station (mainboard + TRACR + ICRR).

2. List of Commands in No Particular Order

<code>enableTracr</code>	Enable TRACR interface. Does not power TRACR on. No indication of success/failure.
<code>disableTracr</code>	Disable TRACR interface. Does not power TRACR off.
<code><command> <value> writev</code>	Write value to virtual address specified by <command>.
<code><command> readv</code>	Read value from virtual address specified by <command> to stack.
<code>ir_getvers</code>	Read firmware version to stack.
<code>ir_datardy</code>	Read data ready bit from ICRR to stack (1 == ready).
<code>ir_oneshot</code>	Software trigger ICRR.
<code>ir_reset</code>	Reset ICRR data readout.
<code><addr> <cnt> ir_rbytes</code>	Read <cnt> bytes of ICRR data to mainboard address <addr>.
<code>ir_housekeep_on</code>	Enable ICRR housekeeping mode (minimal event, no waveform data).
<code>ir_housekeep_off</code>	Disable ICRR housekeeping mode (full waveform readout).
<code>ir_counter_on</code>	Enable ICRR event counter.
<code>ir_counter_off</code>	Disable ICRR event counter.
<code>ir_counter_reset</code>	Reset ICRR event counter.
<code><dac> <ch> <val> ir_writeDAC</code>	Write <val> to staging register for ICRR DAC <dac> (A=0, B=1, etc.), channel <ch> (0-3).
<code>ir_loadDACs</code>	Load all ICRR DACs from staging registers.