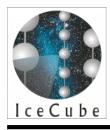


# **DOM-Cal Transit Time Calibration**

John Kelley UW-Madison April 20, 2005

April 20, 2005

DOM-Cal Transit Time J. Kelley

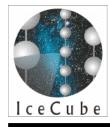


### Motivation

- Signal we measure is actually delayed by:
  - PMT transit time
  - Delay board
- Varies with temperature, voltage, and from DOM to DOM
- Pulser Ch 0 Delay 10b ATWD Ch 1  $\sim 70 \text{ ns}$ FPGA  $\sim 70 \text{ ns}$ Ch 2 10b ATWD 3 Ч 10b fAD MUX 40 MHz **OB-LED**

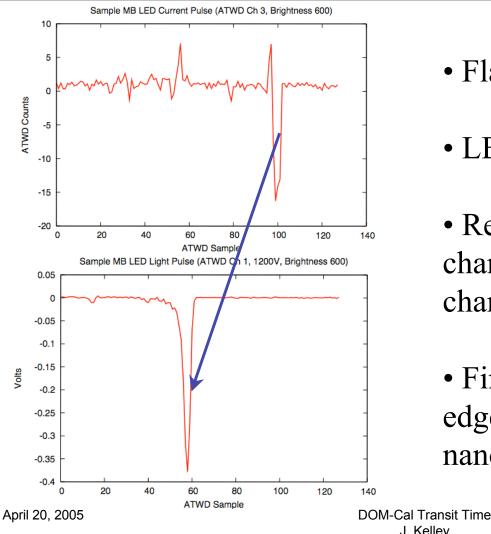
Trigger (2)

• Eternal struggle for truth

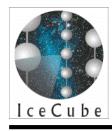


## **Concept of Measurement**

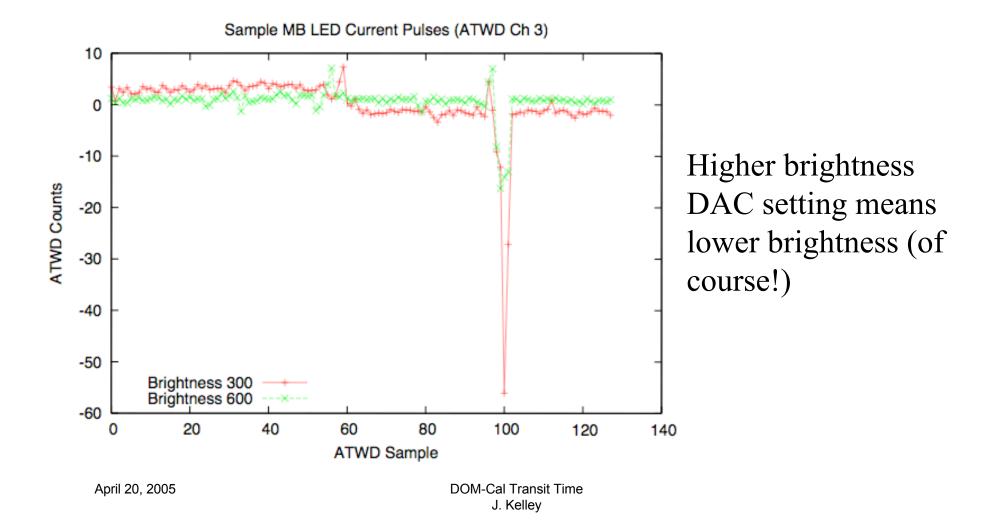
J. Kelley

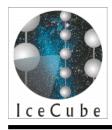


- Flash mainboard LED
- LED-trigger the ATWD
- Record current pulse in channel 3, light pulse in channel 0 or 1
- Find 50% point of leading edges, convert difference to nanoseconds

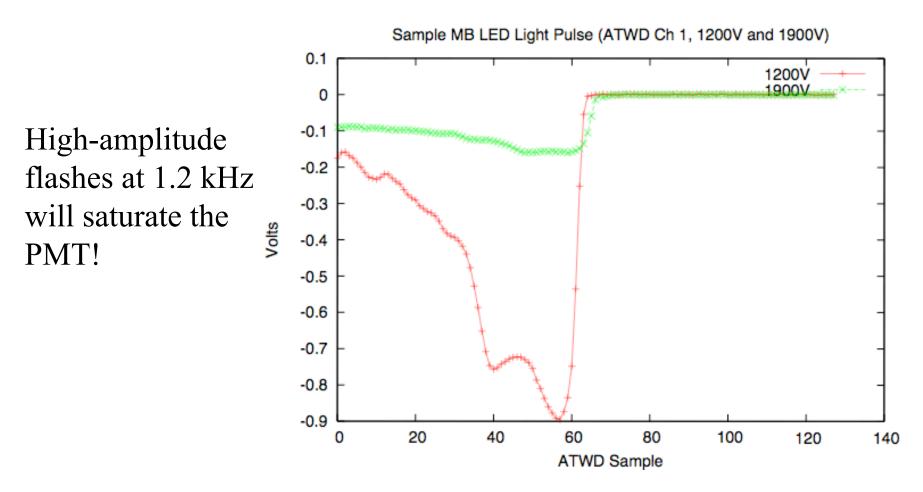


#### Fun "Features"

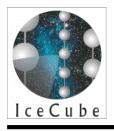




#### More Fun



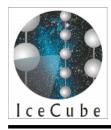
DOM-Cal Transit Time J. Kelley



#### **Even More Fun**

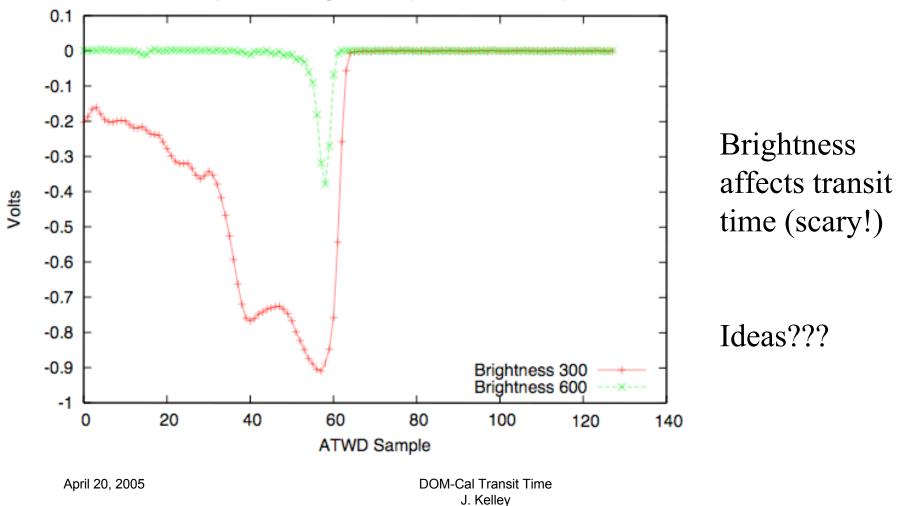
fbdbd436441a\_V **0.01 ⊢** 0.008 0.006 0.004 0.002 0 -0.002 -0.004 -0.006 0 20 40 60 80 100 120 ATWD\_bin April 20, 2005 **DOM-Cal Transit Time** J. Kelley

There is a consistent delay in ATWD channel 0 (black)!



#### **Unbearably Fun**

Sample MB LED Light Pulses (ATWD Ch 1, 1200V)





## **Preliminary Results**

