```vhdl
----------------------------------------------------------------------------
--      VHDL CODE FOR ICECUBE
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--
--      flash_defs.VHD
--      0.00   2/13/2003
--
-- PURPOSE:      Global definitions.
--

----------------------------------------------------------------------------


library IEEE;
use IEEE.STD_LOGIC_1164.all;

package flash_defs is

    subtype address is std_logic_vector(5 downto 0);
    subtype fldata is std_logic_vector(7 downto 0);

--Special Addresses (000xxx)
    constant addr_flags        : address := B"000000";   --flags reg

    constant addr_sys_reset    : address := B"000111";   --system reset



--OneWireMaster (001xxx)
    constant addr_owm_command  : address := B"001000";   --command reg
    constant addr_owm_data     : address := B"001001";   --data TX/RX reg
    constant addr_owm_int      : address := B"001010";   --interrupt reg
    constant addr_owm_inten    : address := B"001011";   --interrupt enable reg
    constant addr_owm_clkdiv   : address := B"001100";   --clock divisor reg
    constant addr_owm_slvsel   : address := B"001101";   --slave select reg
    constant addr_owm_prom      : address := B"001111";   --(W)PROM write pulse

--SPI (010xxx)
    constant addr_spi_active_list : address := B"010001";
    constant addr_spi_operate     : address := B"010010";

--Trigger Delay (101xxx)
--     Each LED module gets 3 bits of data
    constant addr_del_ena       : address := B"101000";   --enable
    constant addr_del_dat0      : address := B"101010";   --module 1, 2
    constant addr_del_dat1      : address := B"101011";   --module 3, 4
    constant addr_del_dat2      : address := B"101100";   --module 5, 6

--"User devices" (11xxxx)

    constant addr_user0         : address := B"110000";
    constant addr_user1         : address := B"110100";
    constant addr_user2         : address := B"111010";
    constant addr_user3         : address := B"111100";


end flash_defs;
```